

BUGC20DH THRU BUGC20KH
● FEATURES

- * Halogen-free type
- * Compliance to RoHS product
- * GPRC (Glass passivated rectifier chip) inside
- * Lead less chip form, no lead damage
- * For surface mounted applications
- * Low power loss, High efficiency
- * Plastic package has Underwriters Laboratory Flammability Classification 94V-0

● APPLICATION

- * General purpose rectification
- * Surge absorption

● MECHANICAL DATA

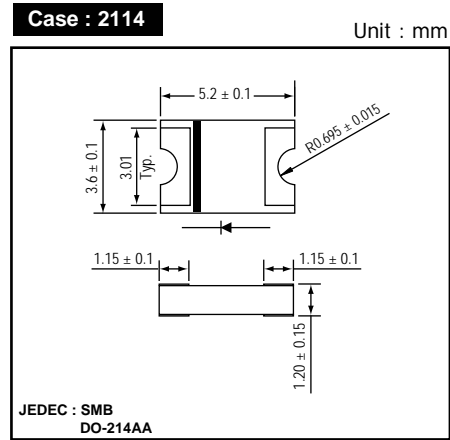
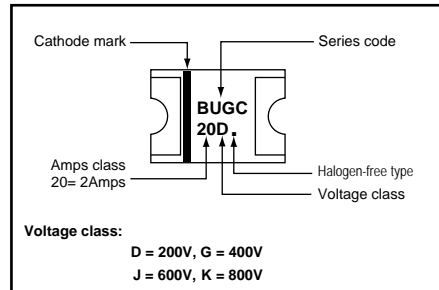
Case : Packed with FRP substrate and epoxy underfilled

Terminals : Pure Tin plated (Lead-Free), solderable per MIL-STD-750, Method 2026.

Polarity : Cathode Band, Laser marking

● PACKING

- * 5,000 pieces per 13" (330mm ± 2mm) reel
- * 2 reels per box
- * 5 boxes per carton

● OUTLINE DIMENSIONS

● MARKING

Absolute Maximum Ratings (Ta = 25 °C)

ITEM	Symbol	Conditions	BUGC20				Unit
			DH	GH	JH	KH	
Repetitive peak reverse voltage	V _{RRM}		200	400	600	800	V
Average forward current	I _{F(AV)}		2.0				A
Peak forward surge current	I _{FSM}	8.3ms single half sine-wave	50				A
Reverse recovery time	T _{rr}	I _F = 0.5A, I _R = 1.0A, I _{rr} = 0.25A	35				nS
Operating storage temperature Range	T _J , T _{STG}		-65 to +175				°C

Electrical characteristics

ITEM	Symbol	Conditions	Type	Min.	Typ.	Max.	Unit
Forward voltage	V _F	I _F = 2.0A	BUGC20DH	-	0.94	0.95	V
			BUGC20GH	-	1.15	1.25	
			BUGC20JH	-	1.40	1.70	
			BUGC20KH	-	1.65	2.20	
Repetitive peak reverse current	I _{RRM}	V _R = Max. V _{RRM} , Ta = 25 °C		-	0.20	5	µA
Junction capacitance	C _j	V _R = 4V, f = 1.0 MHz		-	19	-	pF
Thermal resistance	R _{th(JA)}	Junction to ambient (NOTE)		-	66	-	°C/W
	R _{th(JL)}	Junction to lead (NOTE)		-	8	-	

NOTES : Thermal resistance from junction to ambient and from junction to lead P.C.B. mounted on 0.2 x 0.2" (5.0 x 5.0mm) copper pad areas.

FIG.1 - FORWARD CURRENT DERATING CURVE

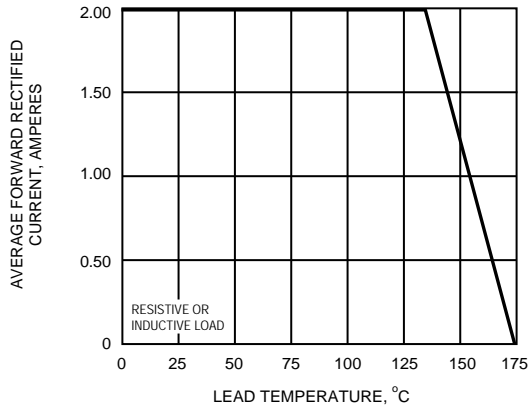


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

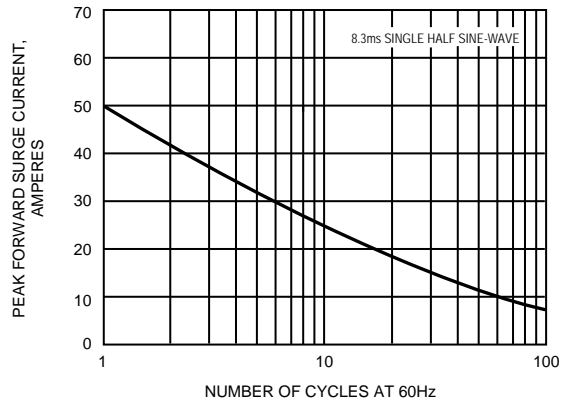


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

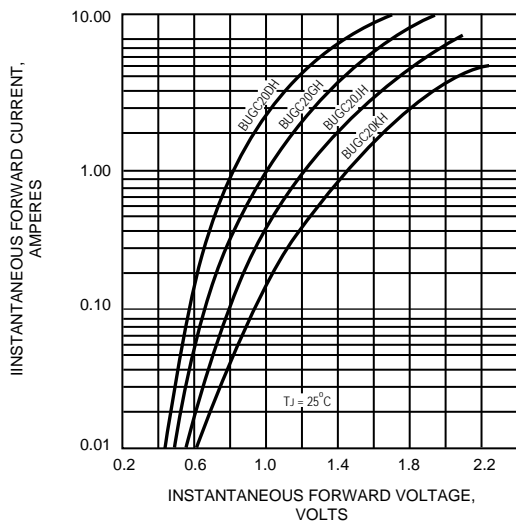


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

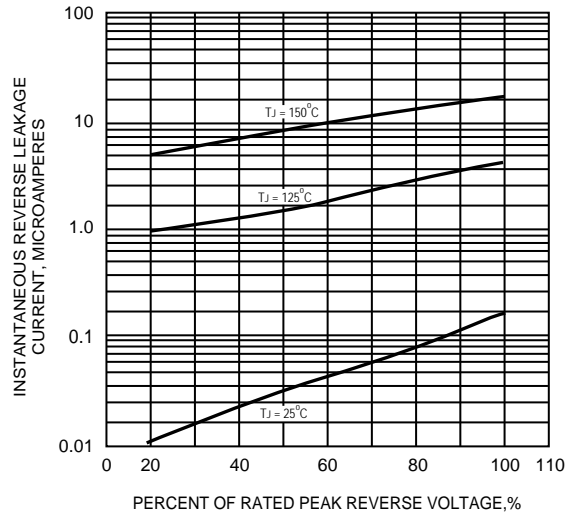


FIG.5 - TYPICAL JUNCTION CAPACITANCE

